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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

pplicant : Hiroaki Yamoto et al.

Group Art Unit 2825

Serial No. : 09/941,396

Filed : August 28, 2001

For : METHOD FOR DESIGN

VALIDATION OF COMPLEX IC

Examiner : Vuthe Siek

<u>AMENDMENT</u>

Hon. Commissioner of Patents and Trademarks Alexandria, VA 22313-1450

Dear Sir:

This amendment is submitted in response to the office action from the U.S. Patent and Trademark Office mailed August 11, 2005 for the above-identified patent application. Please make the following changes: